



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 5042

SERIAL NUMBER 10/615,787	FILING DATE 07/10/2003  RULE	CLASS 324	GROUP ART UNIT 2829	ATTORNEY DOCKET NO. HITA.0413
-----------------------------	---------------------------------------	--------------	------------------------	-------------------------------------

APPLICANTS

Hirotaka Oosawa, Iseaki, JAPAN;  
 Masumi Kasahara, Takasaki, JAPAN;  
 Kazuo Watanabe, Takasaki, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*  
 This application is a CON of 10/036,460 01/07/2002 PAT 6,597,191  
*a*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*  
 JAPAN 2001-006558 01/15/2001  
*m*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 10/03/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 5	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature <i>a</i>	Initials <i>m</i>		

ADDRESS  
 38327  
 REED SMITH LLP  
 3110 FAIRVIEW PARK DRIVE, SUITE 1400  
 FALLS CHURCH , VA  
 22042

TITLE  
 Semiconductor integrated circuit device

FILING FEE  RECEIVED	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
		<input type="checkbox"/> 1.16 Fees ( Filing )
		<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )